

```

graph TD
    6[Raster Processor] -- "8 Raster Data" --> 4[Control Logic]
    4 --- 10[Scan SRAM]
    4 --- 12[LUT SRAM]
    subgraph 12 [LUT SRAM]
        14a[LUT]
        14b[LUT]
        14c[LUT]
        14d[LUT]
    end
    4 --- 18[Pulse Width Modulator PWM]
    18 --- 20[Laser Beam and Print Apparatus]

```

FIG. 2

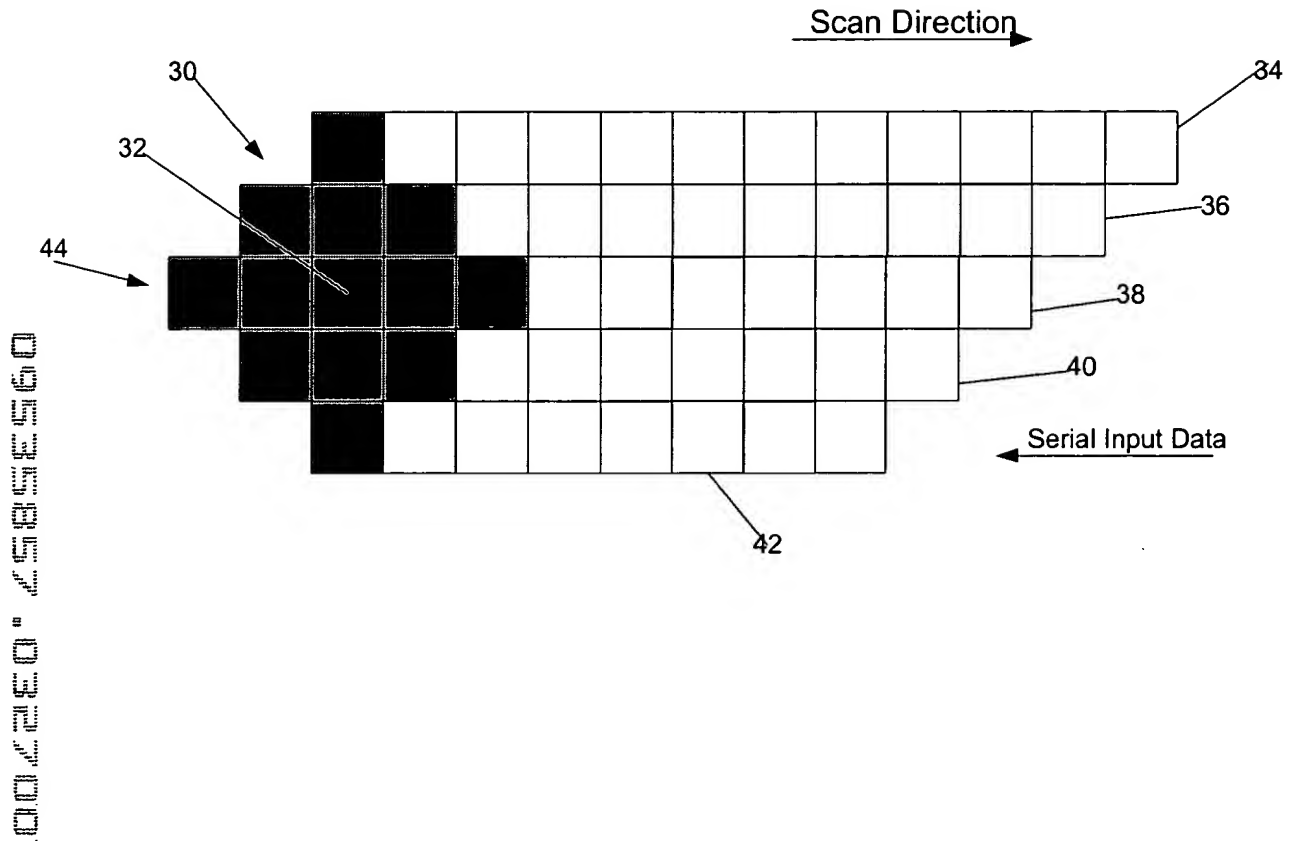


FIG. 3

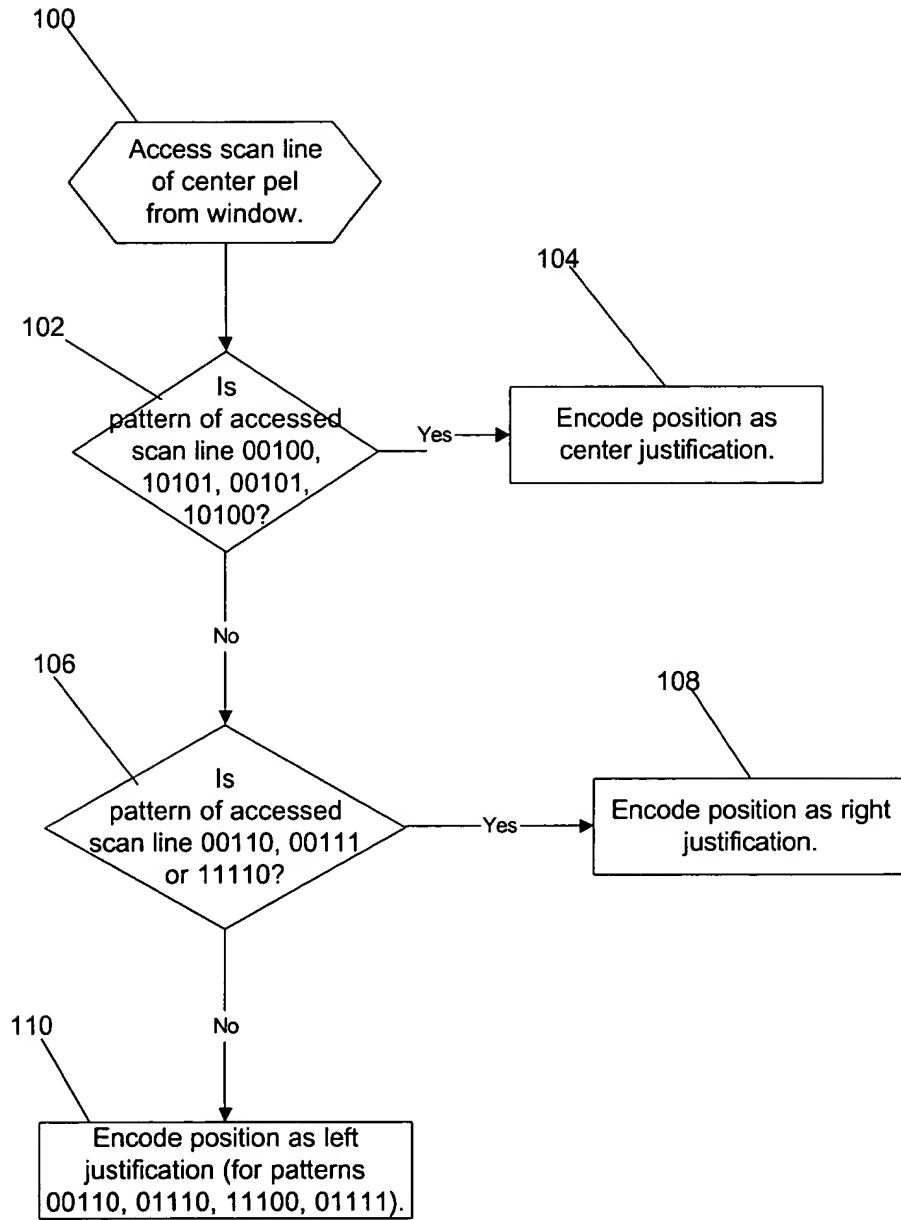


FIG. 4

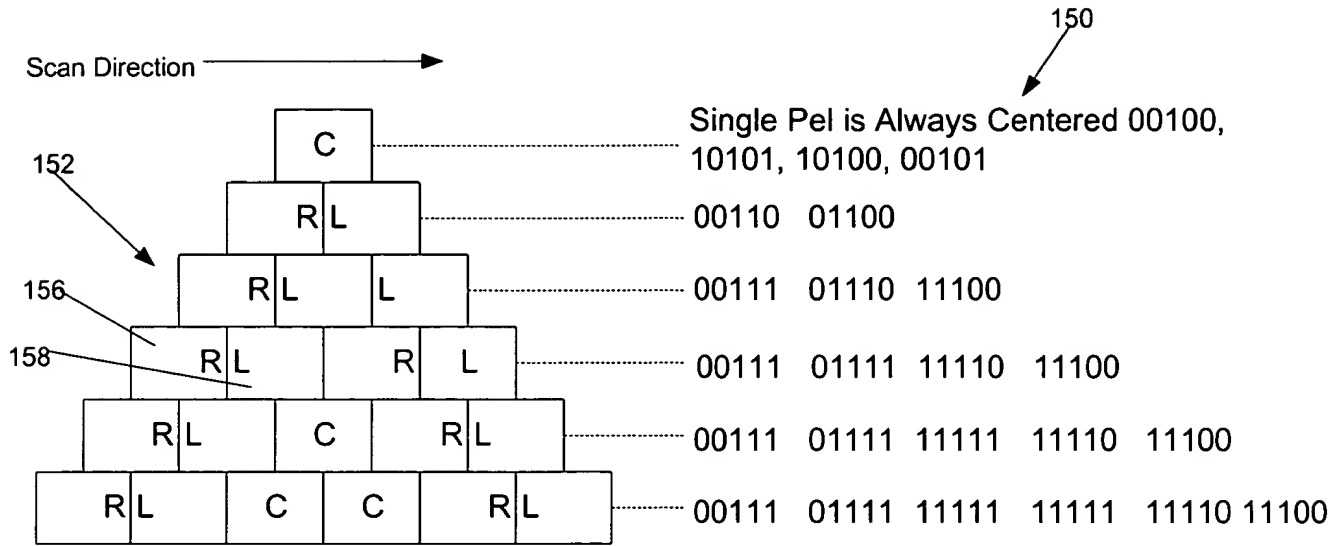


FIG. 5

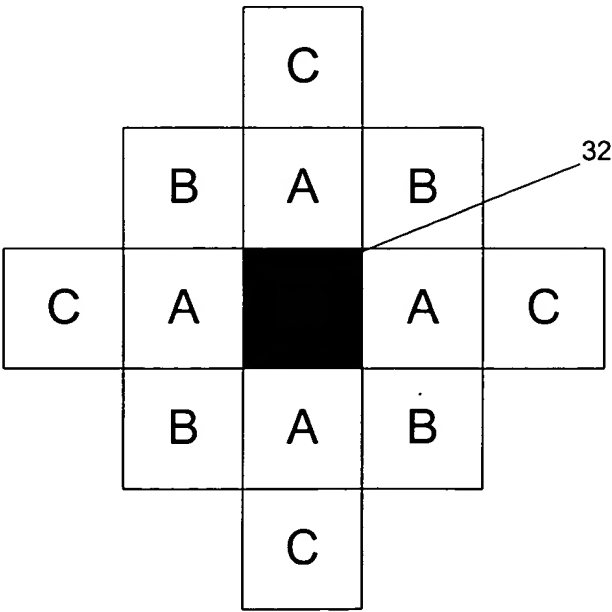


FIG. 6

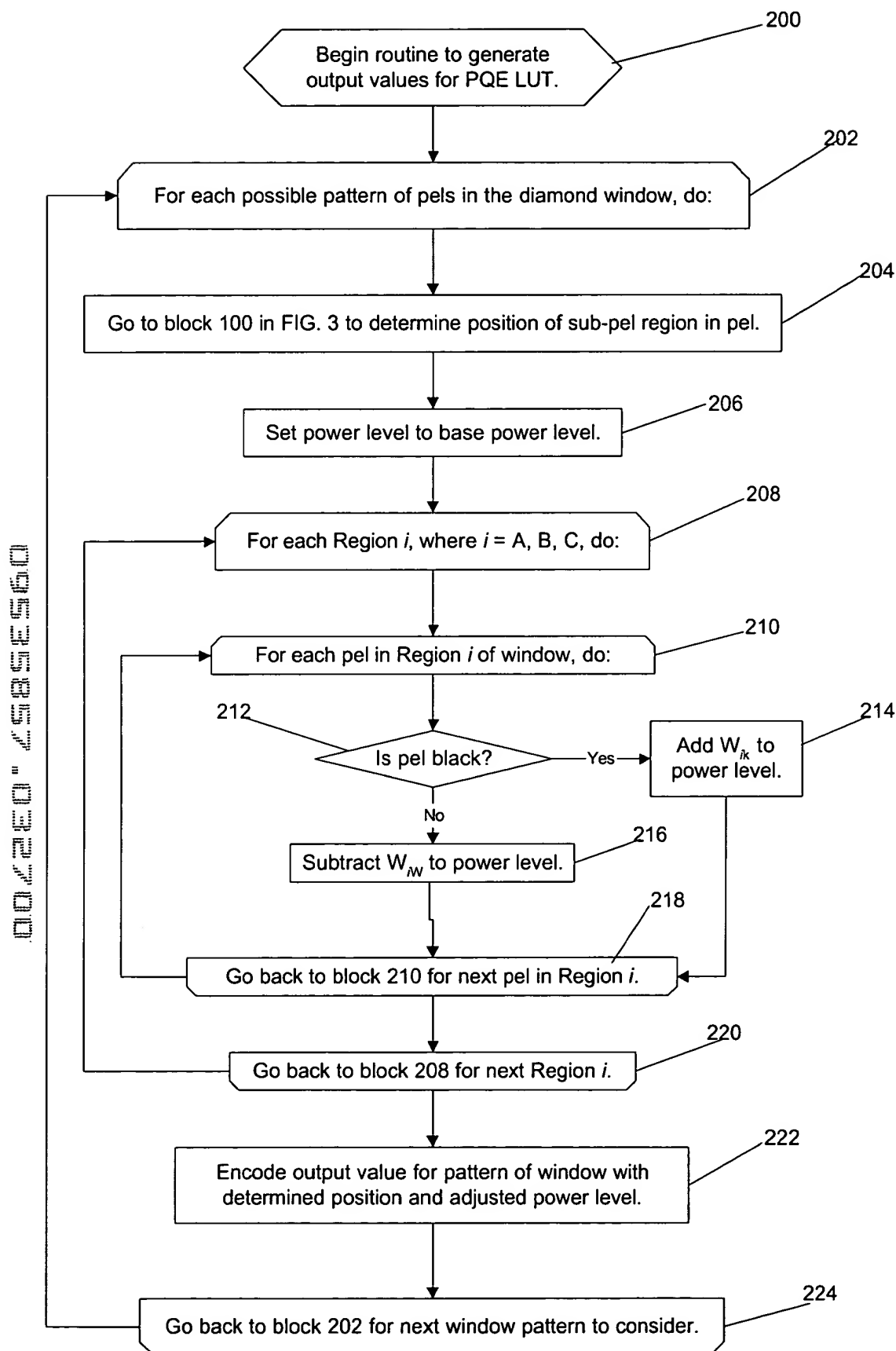


FIG. 7

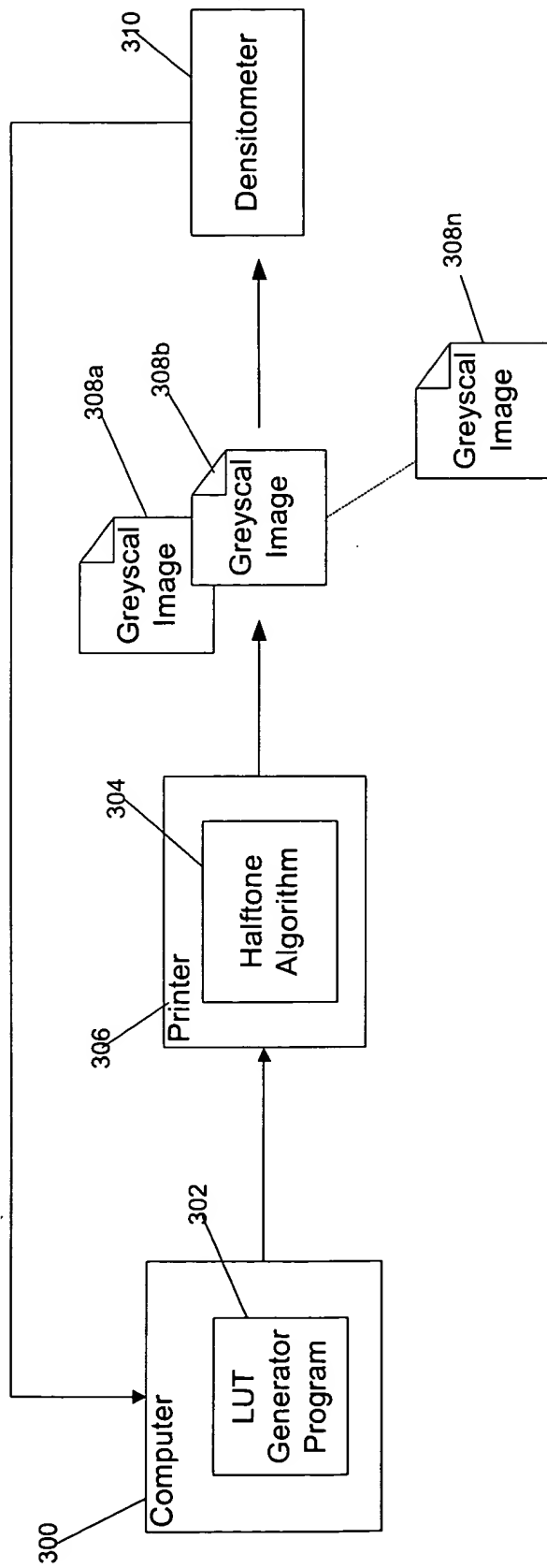
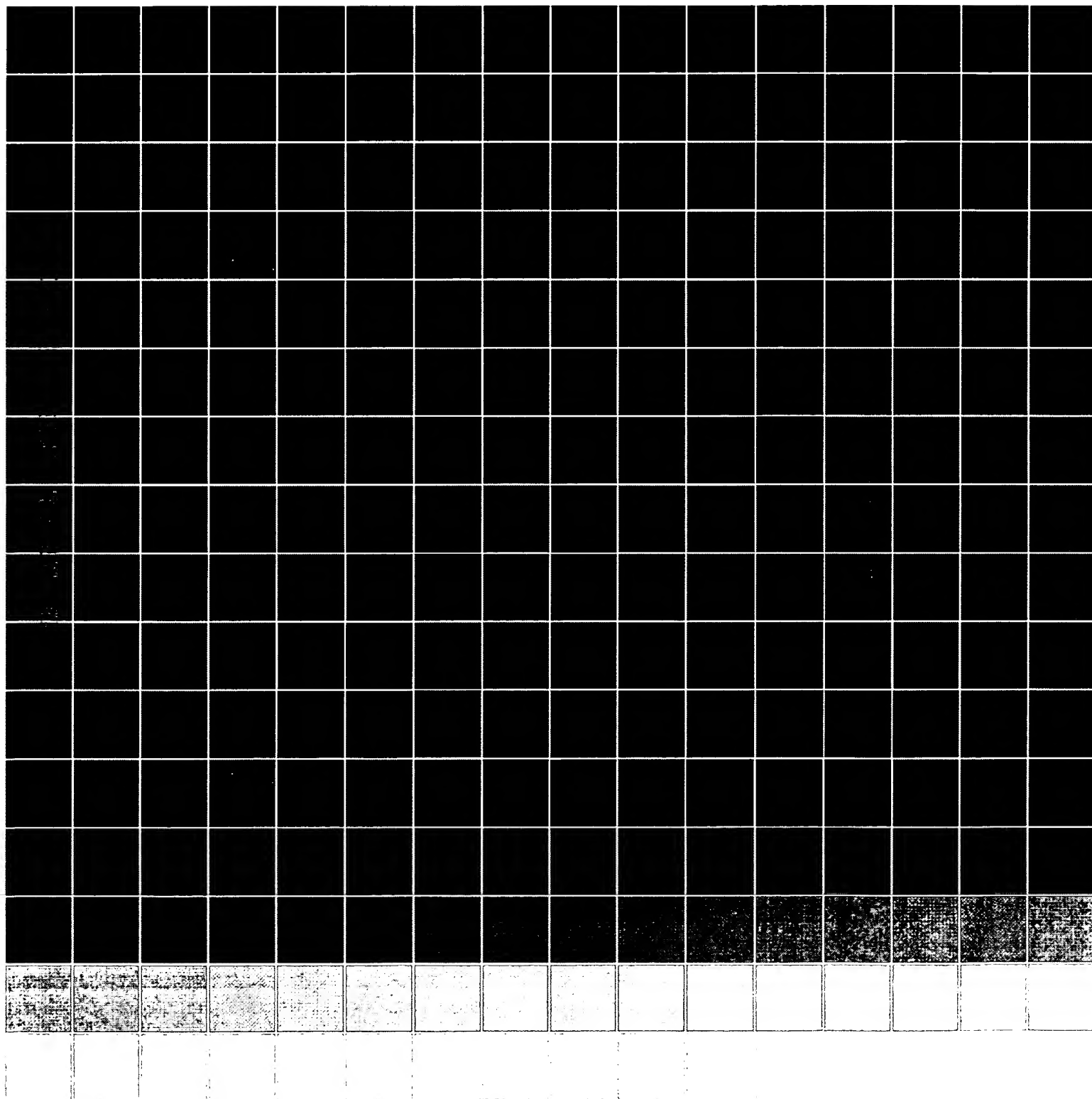


FIG. 8

Prior Art



Input Array +

$$\begin{aligned} \text{Part I} & \left\{ \begin{aligned} & fA(\text{starting_value} - W_{AW}) * \text{Array}_{AW} + fA(\text{starting_value} + W_{AK}) * \text{Array}_{AK} + \\ & fB(\text{starting_value} - W_{BW}) * \text{Array}_{BW} + fB(\text{starting_value} + W_{BK}) * \text{Array}_{BK} + \\ & fC(\text{starting_value} - W_{CW}) * \text{Array}_{CW} + fC(\text{starting_value} + W_{CK}) * \text{Array}_{CK} \end{aligned} \right\} - \\ \text{Part II} & \left\{ fA(\text{starting_value}) * \text{Array}_{AK} + fB(\text{starting_value}) * \text{Array}_{BK} + fC(\text{starting_value}) * \text{Array}_{CK} \right\} + \\ \text{Part III} & \left\{ f0(\text{starting_value} + W_{AK} + W_{BK} + W_{CK} - W_{AW} - W_{BW} - W_{CW}) - f0(\text{starting_value}) \right\} \end{aligned}$$

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FIG. 10

